

Notice of Allowability

Application No.

10/791,341

Examiner

Eric Wendler

Applicant(s)

HUANG, CHENG-YEN

Art Unit

2824

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to the amendment filed on December 13, 2005.
2. ☒ The allowed claim(s) is/are 1-13.
3. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) ☐ All b) ☐ Some* c) ☐ None of the:
 1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

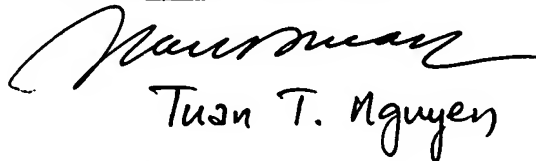
* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
 - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. ☒ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/08), Paper No./Mail Date _____
4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material
5. ☐ Notice of Informal Patent Application (PTO-152)
6. ☐ Interview Summary (PTO-413), Paper No./Mail Date _____
7. ☐ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☒ Other Updated search history.


Tuan T. Nguyen

DETAILED ACTION

1. This action is responsive to the following communications: the applicant's Amendment filed on December 13, 2005.
2. Claims 1-13 are pending in the application. Claims 1, 5, and 11 are independent claims.
3. Acknowledgement is made of the correction to claim 10 to overcome the objection made in the previous office action.

Response to Arguments

4. Applicant's arguments, see pages 8, second paragraph, page 9, first paragraph, and pages 12-13 of the amendment, filed on December 13, 2005, with respect to claims 1, 5 and 11 have been fully considered and are persuasive. The rejections of claims 1, 5, and 11 have been withdrawn.
5. Examiner agrees with the applicant that Toda teaches that both the bit line pairs and the latch node are connected to the same terminal of the transistors, while the claim requires that the latch be connected between the second terminals of the transistors. Examiner also agrees with the applicant that, while Toda teaches a selection unit having two input terminal, one of which is coupled to a flush line, the other input terminal is not coupled to a word line, as required by claim 1, but coupled to a reset signal DCST. Examiner cites that these reasons are sufficient and that the rejection of claim 1 has been withdrawn; as a result, the further arguments submitted by the applicant have been deemed superfluous and have not been addressed. Regarding the rejections of

claims 5 and 11, the arguments applying to claim 1 also apply to claims 5 and 11, and the rejections of claims 5 and 11 are also withdrawn.

Allowable Subject Matter

6. Claims 1-13 are allowed.

7. The following is an examiner's statement of reasons for allowance: the examiner used the best art found in his search to reject the claims in the first office action. The applicant submitted arguments that were persuasive and the rejections were withdrawn. An updated search was performed and claims 1, 5, and 11 were confirmed to be allowable due to the fact that the prior art fails to teach a selection unit having two input terminals coupled to a word line and a flush line, wherein predetermined information is written to the latch node from the bit line pair according to activations of the word line or the flush line. Claims 2-4, 6-10, and 12-13 are also allowable due to their dependence upon an allowable independent claim.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

8. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Cohen et al. (4,567,578) teaches a cache memory containing a selection unit that has a flush line coupled to one input and which propagates a word line signal to a latch. Koike (5,450,353) teaches an SRAM memory in which a selection

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unit has as its inputs a control signal and a reset signal which resets the data in the memory cell after information had been written to the latch from the bit line pair due to activation of the word line.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Eric Wendler whose telephone number is (571) 272-5063. The examiner can normally be reached on Monday - Friday 8AM - 4:30 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Richard Elms can be reached on (571) 272-1869. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

EJW
2/9/06


Tuan T. Nguyen